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## **Nust Engineering**

The JFET gate and drain-source form a pn junction diode; a very simple model

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of the JFET is shown at right. In this model the source to drain resistance depends on the gate bias.

## **Transistor - Wikipedia**

Learning Objectives: This course is an introduction to analog circuits using diodes and transistors. Students will be introduced to simple diode based

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circuits. They will also be introduced to MOS transistors, their characteristics, techniques for biasing them, and amplifiers using them.

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A transistor is a semiconductor device

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used to amplify or switch electronic signals and electrical power. It is composed of semiconductor material usually with at least three terminals for connection to an external circuit. A voltage or current applied to one pair of the transistor's terminals controls the current through another pair of terminals. Because the controlled



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(output) power can be ...

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## **IIT Palakkad**

The MOS capacitor structure is the heart of the MOSFET. Consider a MOS capacitor where the silicon base is of p-type. If a positive voltage is applied at the gate, holes which are at the surface

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of the p-type substrate will be repelled by the electric field generated by the voltage applied.

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